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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Leonard J. Olmer, et al.

Serial No.: 10/675,575

Filed: September 30, 2003

Title: IN-SITU REMOVAL OF SURFACE IMPURITIES PRIOR TO ARSENIC-DOPED POLYSILICON DEPOSITION IN THE FABRICATION OF A HETEROJUNCTION BIPOLAR TRANSISTOR

Grp./A.U.: 1746

Examiner: Jason Mark Heckert

Confirmation No. 9967

For:

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being electronically filed
with United States Patent and Trademark Office on
October 29, 2007

Elizabeth Schumacher
(Printed or typed name of person signing the certificate)

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(Signature of the person signing the certificate)

Sir:

PRELIMINARY AMENDMENT UNDER 37 C.F.R. § 1.114

In response to the Examiner's Action mailed September 18, 2007, please amend the above-identified application as follows: